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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/609,022	06/30/2003	Juha T. Rantala	SLC-017	8411
20374 7590 09/24/2007 KUBOVCIK & KUBOVCIK SUITE 710 900 17TH STREET NW WASHINGTON, DC 20006			EXAMINER CHIMIAK, EMILY ANN	
			ART UNIT 1733	PAPER NUMBER
			MAIL DATE 09/24/2007	DELIVERY MODE PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No. 10/609,022	Applicant(s) RANTALA ET AL.	
	Examiner Emily Chimiak	Art Unit 1733	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 28 June 2007.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 and 2 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1 and 2 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Request for Continued Examination

Applicant's Request for Continued Examination filed on October 6, 2006 is acknowledged by the examiner.

Claim Rejections - 35 USC § 112

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 1 and 2 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. Applicant's amended claim requires that the adhesion promoter be applied to an opto-electronic surface of a device produced in a CMOS process. The specification, as originally filed, only supports a method wherein an adhesion promoter is applied on an electrical surface of a device produced in a CMOS. The specification does not state that the adhesion promoter can be applied on an opto-electronic surface of a device produced in a CMOS process. In fact, paragraph 0131 shows that applicant contemplated applying the adhesion promoter to a surface of an electrical device produced in a CMOS process *as an alternative to* applying the adhesion promoter on an opto-electronic surface. In said paragraph, applicant refers to the example above, which is an example of applying the adhesion promoter on an opto-electronic surface, and then states "however, the adhesion promoter can be used in a CMOS process, such as for increasing g

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adhesion of a dielectric, for increasing adhesion of a passivation layer...” Based upon the specification, the applicant has not demonstrated that at the time the application was filed, applicant contemplated applying the adhesion promoter to an opto-electronic surface of a device produced in a CMOS process.

Response to Arguments

2. Applicant's arguments filed 06/28/2007 have been fully considered but they are not persuasive. As to the argument concerning the 35 U.S.C 112 rejection, examiner acknowledges that the specification need not explicitly state that the adhesion promoter can be applied on an opto-electronic surface of a device produced in a CMOS process. However, as noted in the rejection, described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. Applicant's argument as to why the Specification meets the requirement of 35 U.S.C. 112 is not persuasive. The argument that applicant's disclosure does not exclude using the adhesion promoter on an opto-electronic surface of a device produced in a CMOS process does not provide support for such a step. One of ordinary skill upon reading the original disclosure of applicant would not appreciate that applicant intended to use the CMOS process in combination with opto-electronic devices, that is, the original disclosure does not reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention.

As to the argument regarding the recitation of claim 1, the original claims do not recite a “method of adhering a coating of a polymer, metal, metalloid oxide or fluorinated derivatives thereof to an electrical or opto-electronic surfaced of a device *produced in a CMOS process...*”

The italicized words were not present in the originally filed claims and therefore cannot be used to aid in conveying that applicant was in possession of the invention as of the filing date sought.

3. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Emily Chimiak whose telephone number is (571)272-6486. The examiner can normally be reached on Monday-Friday 8:30-5:30 EST.

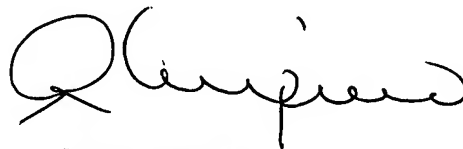
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Crispino can be reached on (571)272-6486. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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EAC



RICHARD CRISPINO
SUPERVISORY PATENT EXAMINER
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